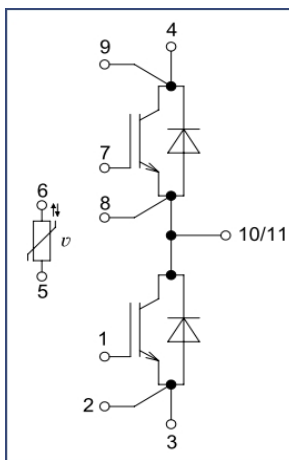
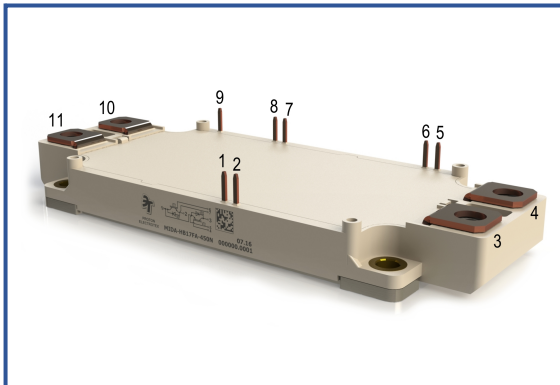


Low Inductance IGBT Module with 17 mm Height Housing
1200 V 600 A

Chip features

- IGBT chip
 - Trench FS — V-Series IGBT (6th gen)
 - low $V_{CE(sat)}$ value
 - 10 μ s short circuit of 150°C
 - square RBSOA of 2xI_C
- FRD chip
 - fast and soft reverse recovery
 - low voltage drop

Design features

- copper baseplate
- Al₂O₃ DBC substrate
- copper wire bonding of power terminals
- Improved thermal cycling
- RoHS compliant
- low inductance value

Typical application

- AC motor drives
- solar inverter
- air conditioning
- high power converters and UPS

Maximum rated values

Definition	Symbol	Conditions	Value	Unit
IGBT				
Collector-Emitter voltage	V_{CES}	$V_{GE} = 0.$	1200	V
Collector current (nominal)	$I_{C\ nom}$		600	A
Repetitive peak collector current* ¹	I_{CRM}	$I_{CRM} = 3 \times I_{C\ nom}; t_p = 1\ ms.$	1800	A
Short-circuit duration	t_{psc}	$T_{vj} = 25^\circ C; V_{GE} = \pm 15\ V; V_{CE} = 720\ V;$ $R_{G\ on} = R_{G\ off} = 1.5\ \Omega.$	10	μ s
		$T_{vj} = 150^\circ C; V_{GE} = \pm 15\ V; V_{CE} = 720\ V;$ $R_{G\ on} = R_{G\ off} = 1.5\ \Omega.$	10	
Gate-Emitter voltage	V_{GES}		± 20	V
Junction operating temperature	$T_{vj\ (op)}$		-40...+150	°C
Inverse diode				
Repetitive peak reverse voltage	V_{RRM}	$V_{GE} = 0\ V.$	1200	V
Forward current (nominal)	$I_{F\ nom}$		600	A
Repetitive peak forward current* ¹	I_{FRM}	$I_{FRM} = 3 \times I_{F\ nom}; t_p = 1\ ms.$	1800	A
Junction operating temperature	$T_{vj\ (op)}$		-40...+150	°C
Module				
Storage temperature	T_{stg}		-55...+50	°C
Isolation voltage	V_{isol}	AC sin 50 Hz; t = 1 min.	4000	V

*¹ Pulse width and repetition rate should be such that device junction temperature does not exceed maximum T_{vj} rating

Characteristics

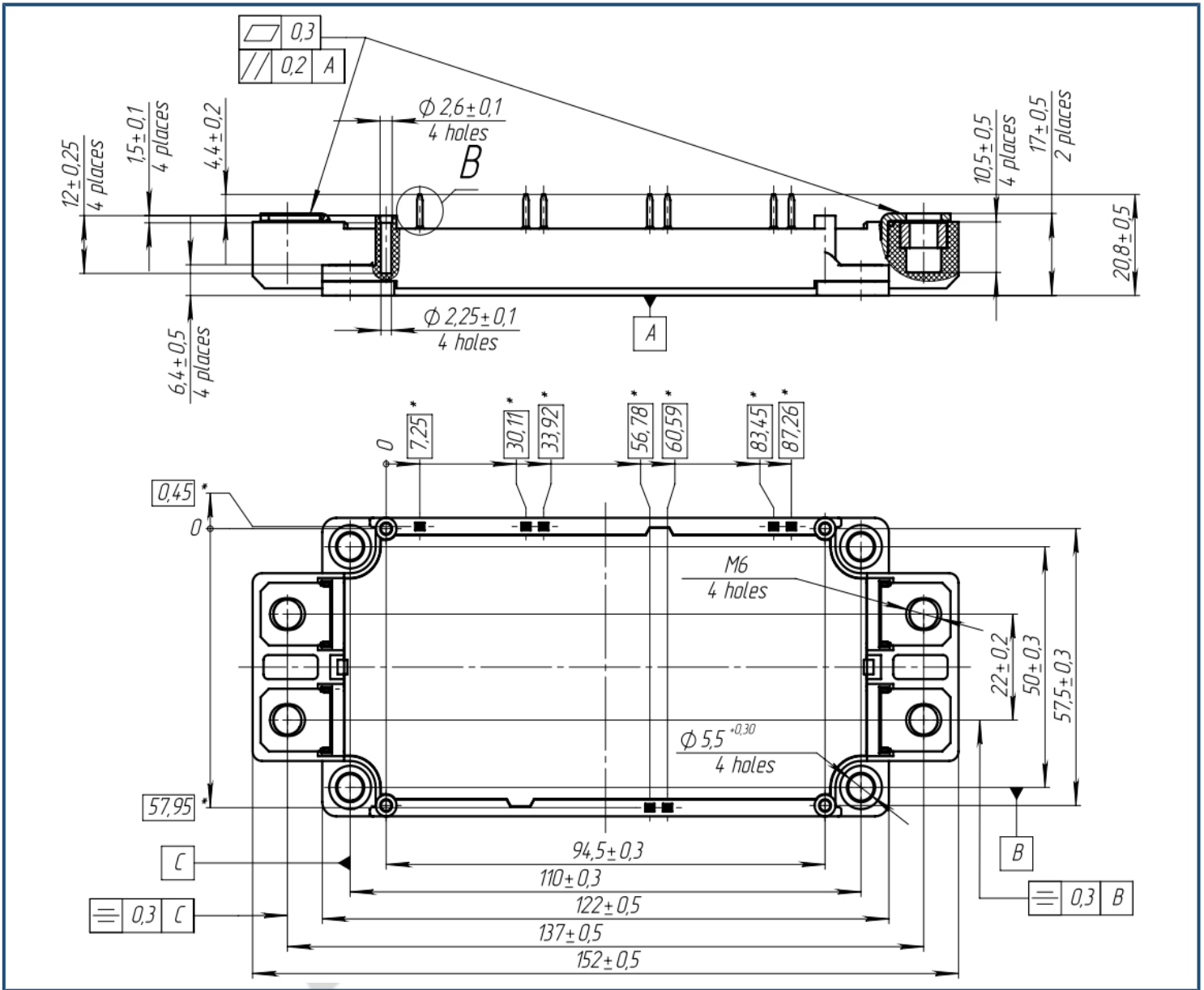
Definition	Symbol	Conditions	Value			Unit.	
			min.	typ.	max.		
IGBT							
Collector-Emitter saturation voltage	V_{CEsat}	$V_{GE} = +15\text{ V}; I_C = 600\text{ A}; t_u = 1000\ \mu\text{s}.$	$T_{vj} = 25^\circ\text{C}$	2.30		V	
			$T_{vj} = 150^\circ\text{C}$	3.00		V	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$I_C = 24\text{ mA}; V_{CE} = V_{GE}; T_{vj} = 25^\circ\text{C}; t_u = 2\text{ ms}.$		5.84		V	
Collector-Emitter cut-off current	I_{CES}	$V_{CE} = 600\text{ V}; t_u = 50\text{ ms}; V_{GE} = 0.$	$T_{vj} = 25^\circ\text{C}$	3.00	300	μA	
			$T_{vj} = 150^\circ\text{C}$	2.50	3.00	mA	
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0; V_{GE} = \pm 20\text{ V}; T_{vj} = 25^\circ\text{C}; t_u = 30\text{ ms}.$		9.00	600	nA	
Input capacitance	C_{ies}	$V_{CE} = 10\text{ V}; V_{GE} = 0\text{ V}; f = 1\text{ MHz}; T_{vj} = 25^\circ\text{C}.$		50.0		nF	
Output capacitance	C_{oes}			3.60		nF	
Reverse transfer capacitance	C_{res}			4.20		nF	
Total gate charge	Q_G	$I_C = 600\text{ A}; V_{CE} = 600\text{ V}; V_{GE} = -8\div 15\text{ V}.$				nC	
Internal gate resistance	R_{Gint}	$T_{vj} = 25^\circ\text{C}.$		1.25		Ω	
Turn-on delay time	$t_{d(on)}$	$V_{CE} = 600\text{ V}; V_{GE} = \pm 15\text{ V}; I_{Cmax} = 600\text{ A}; R_G = 1.5\ \Omega; L = 30\ \mu\text{H}.$	$T_{vj} = 25^\circ\text{C}$	330		ns	
			$T_{vj} = 150^\circ\text{C}$	497			
Rise time	t_{ri}		$T_{vj} = 25^\circ\text{C}$	109		ns	
			$T_{vj} = 150^\circ\text{C}$	101			
Turn-on energy	E_{on}		$T_{vj} = 25^\circ\text{C}$	27.3		mJ	
			$T_{vj} = 150^\circ\text{C}$	29.5			
Turn-off delay time	$t_{d(off)}$		$T_{vj} = 25^\circ\text{C}$	800		ns	
			$T_{vj} = 150^\circ\text{C}$	788			
Fall time	t_{fi}		$T_{vj} = 25^\circ\text{C}$	119		ns	
			$T_{vj} = 150^\circ\text{C}$	201			
Turn-off energy	E_{off}	$T_{vj} = 25^\circ\text{C}$	66.2		mJ		
		$T_{vj} = 150^\circ\text{C}$	82.7				
Collector-emitter threshold voltage	V_{CE0}	$V_{GE} = +15\text{ V}; T_{vj} = 150^\circ\text{C};$		0.86		V	
On-State slope resistance (IGBT)	r_{CE0}	$I_{CE1} = 150\text{ A}; I_{CE2} = 600\text{ A}; t_u = 1000\ \mu\text{s}.$		3.50		$\text{m}\Omega$	
Thermal resistance junction to case	$R_{th(j-c)}$	DC; $I_{CE} = 550\pm 50\text{ A}; I_{test} = 1.5\text{ A}; V_{GE} = +15\text{ V}.$		0.04		K/W	
Inverse diode							
Forward voltage drop	V_F	$I_F = 600\text{ A}; V_{GE} = 0; t_u = 1000\ \mu\text{s}.$	$T_{vj} = 25^\circ\text{C}$	2.30		V	
			$T_{vj} = 150^\circ\text{C}$	2.60		V	
Reverse recovery time	t_{rr}	$V_{CE} = 600\text{ V}; V_{GE} = \pm 15\text{ V}; I_{Cmax} = 600\text{ A}; R_G = 1.5\ \Omega; L = 30\ \mu\text{H}.$	$T_{vj} = 25^\circ\text{C}$	163		ns	
			$T_{vj} = 150^\circ\text{C}$	318		ns	
Repetitive peak reverse current	I_{RRM}		$T_{vj} = 25^\circ\text{C}$	344		A	
			$T_{vj} = 150^\circ\text{C}$	490		A	
Reverse recovered charge	Q_{rr}		$T_{vj} = 25^\circ\text{C}$	34.1		μC	
			$T_{vj} = 150^\circ\text{C}$	78.1		μC	
Reverse recovery energy	E_{rec}		$T_{vj} = 25^\circ\text{C}$	18.7		mJ	
			$T_{vj} = 150^\circ\text{C}$	40.4		mJ	
Threshold voltage	$V_{(T0)}$		$T_{vj} = 150^\circ\text{C}; V_{GE} = 0; I_{CE1} = 150\text{ A};$		0.86		V
Forward slope resistance	r_T		$I_{CE2} = 600\text{ A}; t_u = 1000\ \mu\text{s}$		2.80		$\text{m}\Omega$
Thermal resistance junction to case	$R_{th(jc-D)}$	DC; $I_{CE} = 350\pm 50\text{ A}; I_{test} = 1.5\text{ A}; V_{GE} = +15\text{ V}.$		0.06		K/W	

Module							
Pin resistance	R_{Pxy}	$T_{vj} = 25^{\circ}\text{C}.$	R_{P12}			1.00	m Ω
			R_{P13}			1.00	
Parasitic inductance between terminals	L_{Pxy}	$T_{vj} = 25^{\circ}\text{C};$ $f = 1 \text{ MHz}.$	L_{P12}			20.0	nH
			L_{P13}			20.0	
Thermal resistance case to heatsink	R_{thCH}	per module			0.009	0.014	K/W
Mounting torque for screws to heatsink	M_s	to heatsink M6		3		6	N*m
Mounting torque for terminal screws	M_t	to terminals M5		3		6	N*m
Weight	W				355		g

Notes:

- Insulating material operating temperature 125°C max;
- Case temperature 125°C max;
- The recommended operating junction temperature $T_{vj\ op} = -40 \div +150^{\circ}\text{C}.$

Advance Data

Overall dimensions: Package type – DA

Part numbering guide

MIDA	-	HB	12	FA	-	600	N	
MIDA								IGBT module package type: DA
		HB						2 switches as Half-Bridge
			12					Voltage rating ($V_{CES}/100$)
				FA				IGBT+FRD chipset modification
						600		Current Rating
							N	Climatic version: normal climate

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